

WDJ26XX

LOW DROPOUT LINEAR REGULATOR

GENERAL DESCRIPTION

WDJ26XX series are a set of Low Dropout Linear Regulator ICs implemented in CMOS technology. They can withstand voltage 40V. And they are available with low voltage drop and low quiescent current, widely used in audio, video and communication appliances.

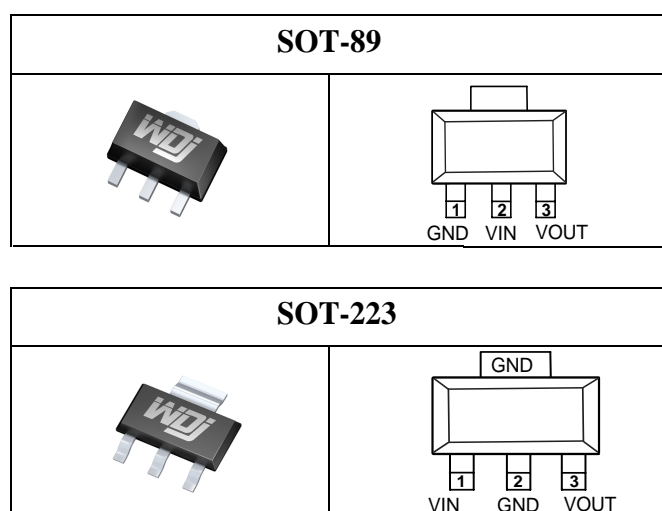
FEATURES

- Low Power Consumption
- Low Voltage Drop
- Low Temperature Coefficient
- Withstanding Voltage 40V
- Quiescent Current 1.5 μ A
- Output Voltage Accuracy: tolerance $\pm 2\%$
- High output current: 250mA
- Temperature exceeds 110 °C output current decreases

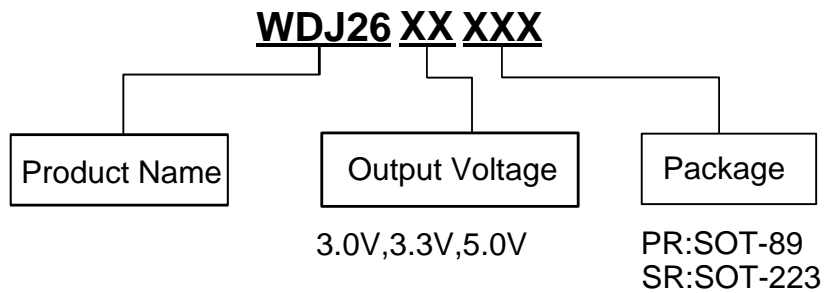
TYPICAL APPLICATIONS

- Battery-powered Equipments
- Communication Equipments
- Audio/Video Equipments
- Smart Battery Packs
- Smoke Detectors
- CO2 DETECTORS

PIN CONFIGURATION



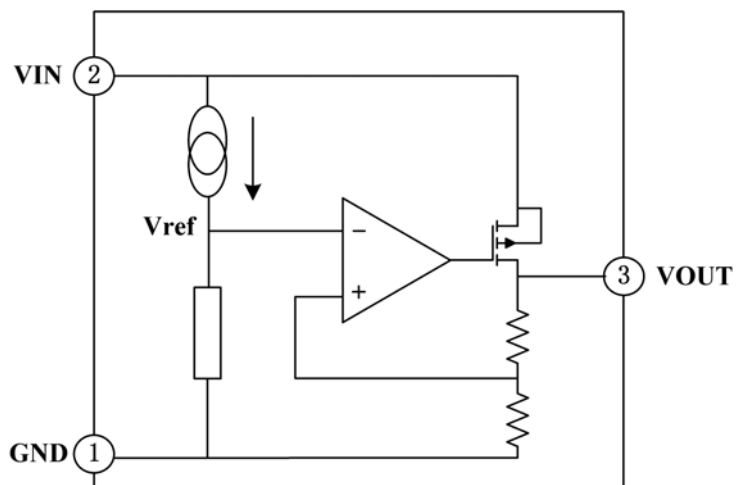
ORDERING INFORMATION



PIN DESCRIPTION

PIN No.		Name	Functions Description
SOT-89	SOT-223		
1	2	GND	ground
2	1	V _{IN}	input
3	3	V _{OUT}	output

FUNCTIONAL BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Description	Symbol	Value range	Unit
Limit Power Voltage	V _{IN}	−0.3~+43	V
Storage Temperature Range	T _{STG}	−50~+125	°C
Operating Free-air Temperature Range	T _A	−40~+85	°C

Note : Stresses greater than those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “Recommended Operating Conditions” is not implied. Exposure to “Absolute Maximum Ratings” for extended periods may affect device reliability.

HEAT DISSIPATION

Description	Symbol	Package	Value range	Unit
Thermal resistance	θ_{JA}	SOT-89	200	°C/W
		SOT-223	150	°C/W
Power dissipation	P _W	SOT-89	500	mW
		SOT223	600	mW

DC CHARACTERISTICS (unless otherwise noted $T_A = +25^\circ\text{C}$)

Series WDJ2630

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_{OUT}	$V_{IN}=V_{OUT}+2.0\text{V}$, $I_{OUT}=10\text{mA}$	2.94	3.00	3.06	V
Output Current	I_{OUT}	$V_{IN}=V_{OUT}+2.0\text{V}$	70	250	—	mA
Load Regulation	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0\text{V}$ $1\text{mA} \leq I_{OUT} \leq 100\text{mA}$	—	—	40	mV
Voltage Drop	V_{DIF}	$I_{OUT}=1\text{mA}$, $\Delta V_{OUT}=2\%$	—	—	55	mV
Quiescent Current	I_{SS}	No Load	—	1.5	3.0	μA
Line Regulation	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \frac{\Delta V_{IN}}{\Delta V_{IN}}$	$V_{OUT}+1.0\text{V} \leq V_{IN} \leq 40\text{V}$, $I_{OUT}=1\text{mA}$	—	—	0.2	%/V
Input Voltage	V_{IN}	—	—	—	40	V
Temperature Coefficient	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0\text{V}$, $I_{OUT}=10\text{mA}$, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	—	100	—	ppm/ $^\circ\text{C}$

Note : When $V_{IN}=V_{OUT}+2.0\text{V}$, as the output voltage declined 2%, the $V_{DIF}=V_{IN}-V_{OUT}$.

Series WDJ2633

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_{OUT}	$V_{IN}=V_{OUT}+2.0\text{V}$, $I_{OUT}=10\text{mA}$	3.234	3.30	3.366	V
Output Current	I_{OUT}	$V_{IN}=V_{OUT}+2.0\text{V}$	70	250	—	mA
Load Regulation	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0\text{V}$ $1\text{mA} \leq I_{OUT} \leq 100\text{mA}$	—	—	40	mV
Voltage Drop	V_{DIF}	$I_{OUT}=1\text{mA}$, $\Delta V_{OUT}=2\%$	—	—	55	mV
Quiescent Current	I_{SS}	No Load	—	1.5	3.0	μA
Line Regulation	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \frac{\Delta V_{IN}}{\Delta V_{IN}}$	$V_{OUT}+1.0\text{V} \leq V_{IN} \leq 40\text{V}$, $I_{OUT}=1\text{mA}$	—	—	0.2	%/V
Input Voltage	V_{IN}	—	—	—	40	V
Temperature Coefficient	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0\text{V}$, $I_{OUT}=10\text{mA}$, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	—	100	—	ppm/ $^\circ\text{C}$

Note : When $V_{IN}=V_{OUT}+2.0\text{V}$, as the output voltage declined 2%, the $V_{DIF}=V_{IN}-V_{OUT}$.

Series WDJ2650

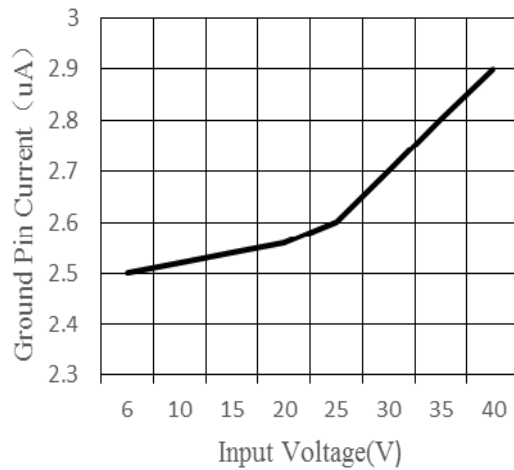
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output Voltage	V_{OUT}	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$	4.9	5.0	5.1	V
Output Current	I_{OUT}	$V_{IN}=V_{OUT}+2.0V$	100	250	—	mA
Load Regulation	ΔV_{OUT}	$V_{IN}=V_{OUT}+2.0V$ $1mA \leq I_{OUT} \leq 100mA$	—	—	40	mV
Voltage Drop	V_{DIF}	$I_{OUT}=1mA$, $\Delta V_{OUT}=2\%$	—	—	55	mV
Quiescent Current	I_{SS}	No Load	—	1.5	3.0	μA
Line Regulation	$\frac{\Delta V_{OUT}}{V_{OUT}} \cdot \frac{\Delta V_{IN}}{V_{IN}}$	$V_{OUT}+1.0V \leq V_{IN} \leq 40V$, $I_{OUT}=1mA$	—	—	0.2	%/V
Input Voltage	V_{IN}	—	—	—	40	V
Temperature Coefficient	$\frac{\Delta V_{OUT}}{\Delta T_A} \cdot V_{OUT}$	$V_{IN}=V_{OUT}+2.0V$, $I_{OUT}=10mA$, $-40^{\circ}C \leq T_A \leq 85^{\circ}C$	—	100	—	ppm/ $^{\circ}C$

Note : When $V_{IN}=V_{OUT}+2.0V$, as the output voltage declined 2%, the $V_{DIF}=V_{IN}-V_{OUT}$.

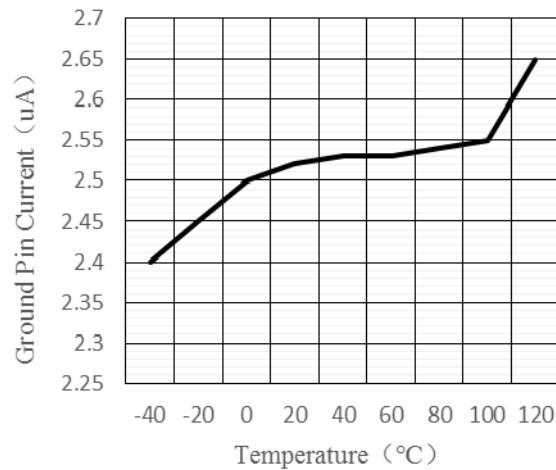
TYPICAL PERFORMANCE CURVES

Test conditions $V_{IN} = 7V$, $V_{OUT} = 5V$, $C1 = C2 = 10\mu F$, $T_A = 25^\circ C$

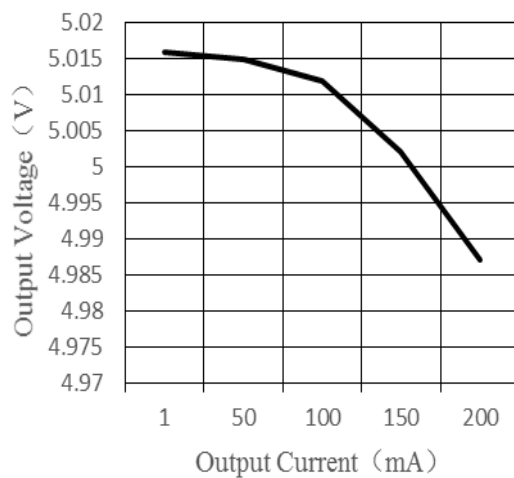
Ground Pin Current Vs Input Voltage



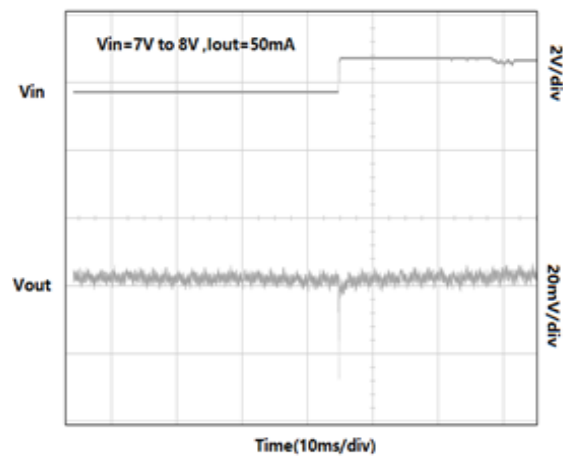
Ground Pin Current Vs Temperature



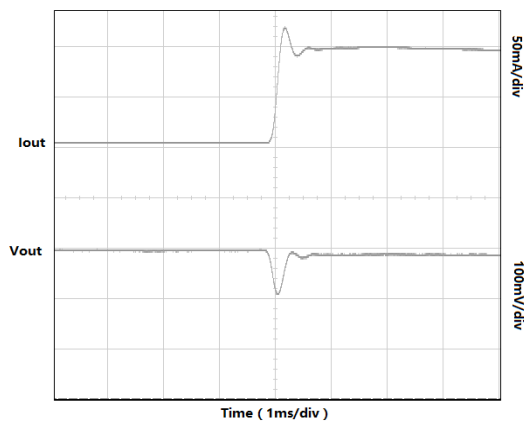
Output Voltage Vs Output Current



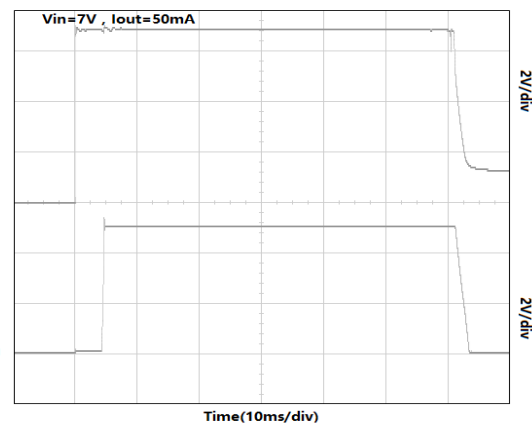
Line-transient Response



Load transient Response



Power Up/Down Output Waveform



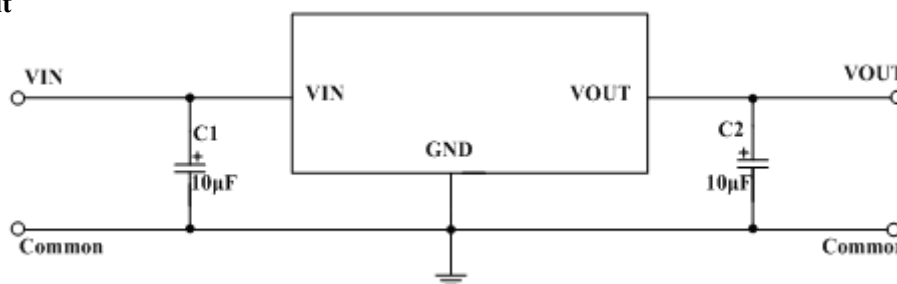
FUNCTIONAL DESCRIPTION

WDJ26XX series are linear voltage regulator ICs withstanding 40V voltage. The series IC consists of a voltage reference, an error amplifier, a current limiter and a phase compensation circuit plus a driver transistor. The output stabilization capacitor is also compatible with low ESR ceramic capacitors.

The over current protection circuit and the over voltage protection circuit are built-in. The protection circuit will operate when the output current or input voltage reaches limit level.

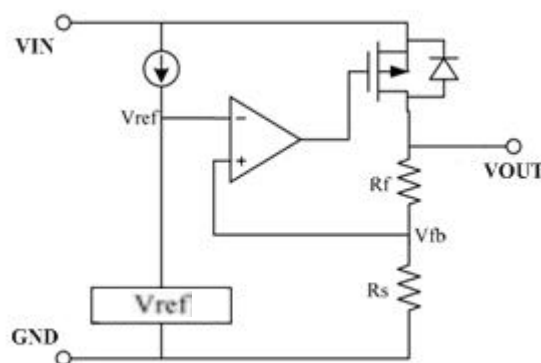
TYPICAL APPLICATION CIRCUIT

Basic Circuit



APPLICATION DESCRIPTION

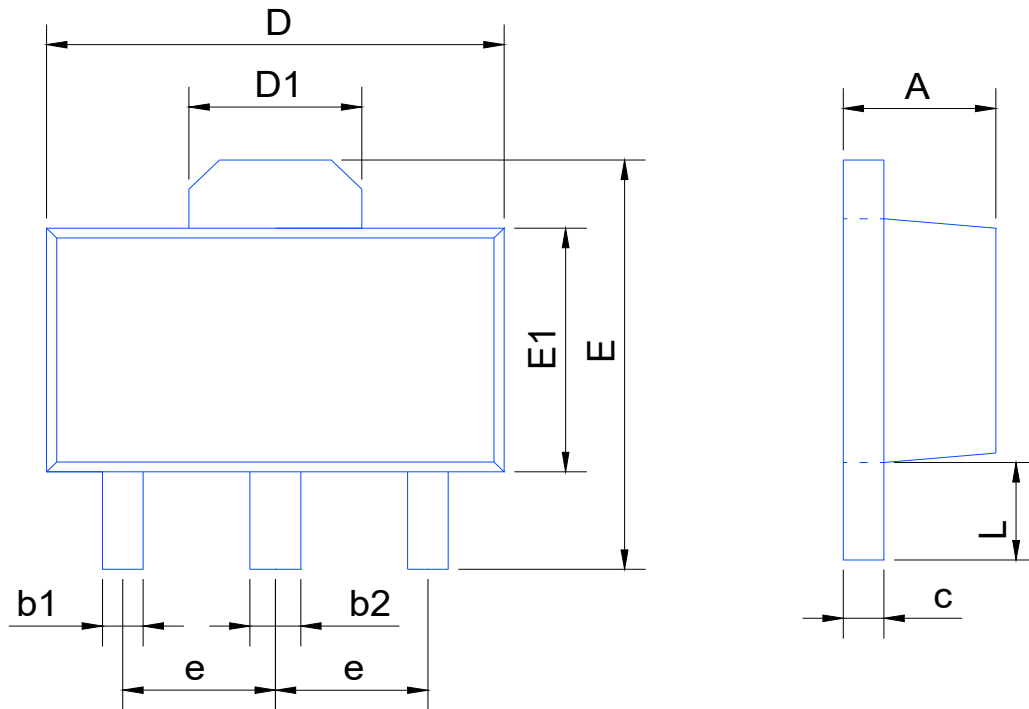
The error amplifier compares the input voltage of the divider resistor composed of feedback resistors R_s and R_f with the reference voltage V_{ref} , and provides the necessary gate voltage to the output transistor through this error amplifier, so that the output voltage is not affected by input voltage or temperature changes and remains constant.



1. When applying, try to connect the capacitor near the VIN and VOUT pins.
2. A phase compensation circuit is used inside the circuit and the ESR of the output capacitor is used for compensation. Therefore, the output to ground must be connected to a capacitor 2.2µF, larger than , and tantalum capacitors are recommended.
3. Pay attention to the usage conditions of input and output voltage and load current to avoid the power consumption inside the IC exceeding the maximum power consumption allowed by the package.

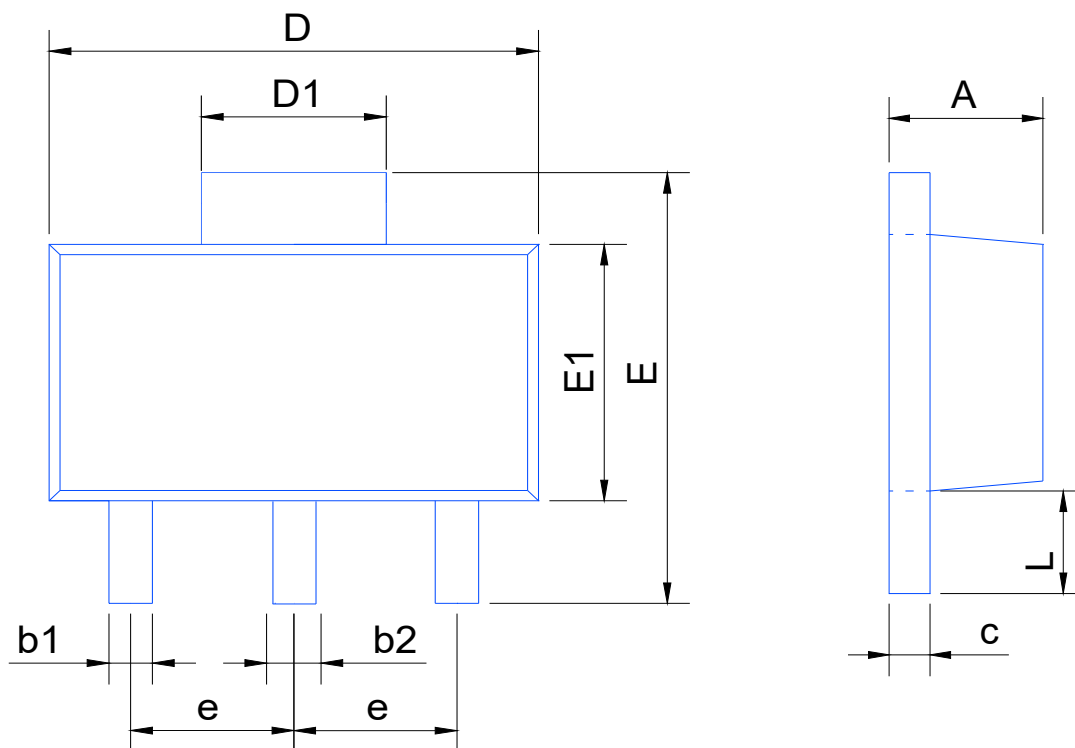
PACKAGE INFORMATION

SOT-89



SYMBOL	mm	
	min	max
A	1.40	1.60
b1	0.35	0.50
b2	0.45	0.60
c	0.36	0.46
D	4.30	4.70
D1	1.40	1.80
E	4.00	4.40
E1	2.30	2.70
e	1.50BSC	
L	0.80	1.20

SOT-223



SYMBOL	mm	
	min	max
A	1.52	1.80
b1	0.66	0.84
b2	0.66	0.84
c	0.23	0.35
D	6.45	6.85
D1	2.9	3.0
E	6.83	7.07
E1	3.45	3.85
e	2.3BSC	
L	0.90	1.150

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